

FIG. 1

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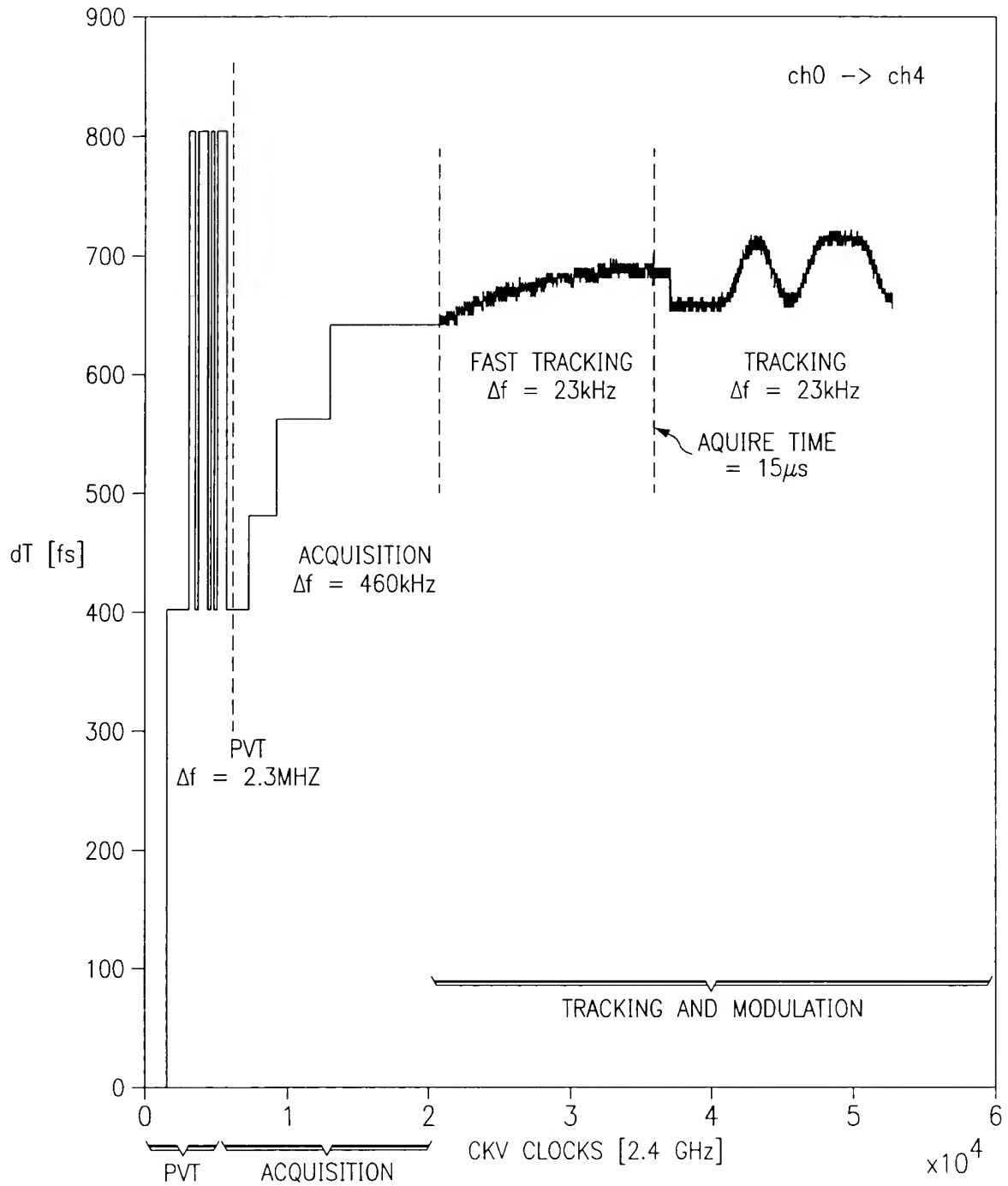


FIG. 2

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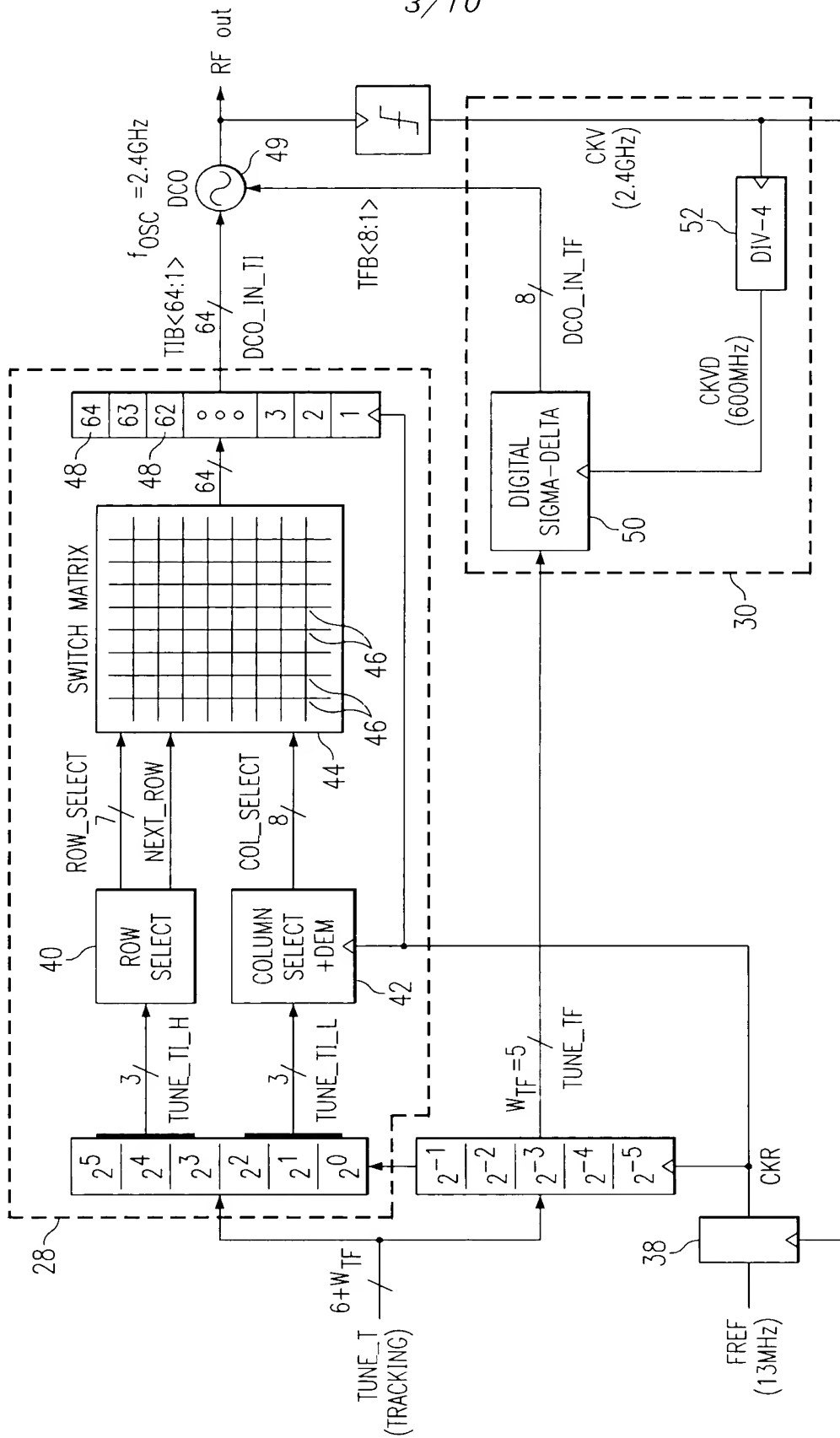
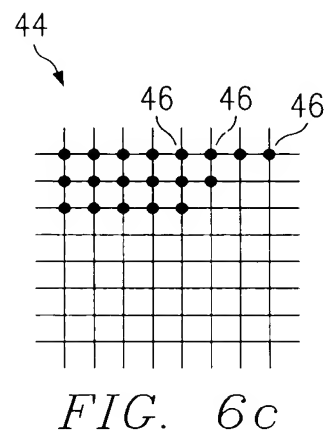
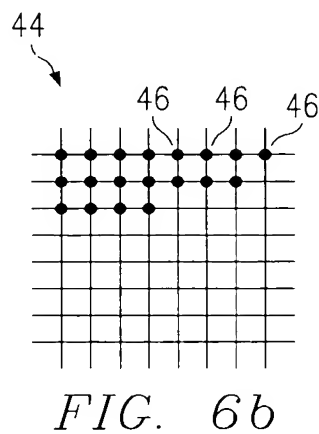
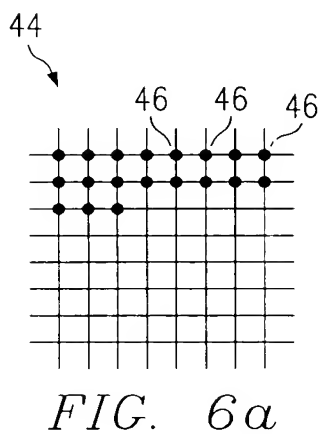
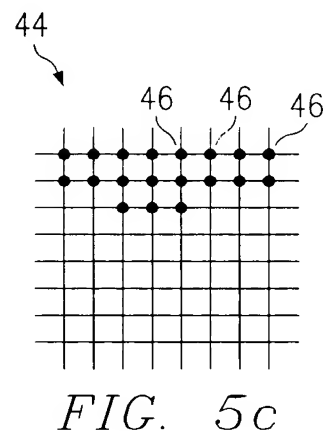
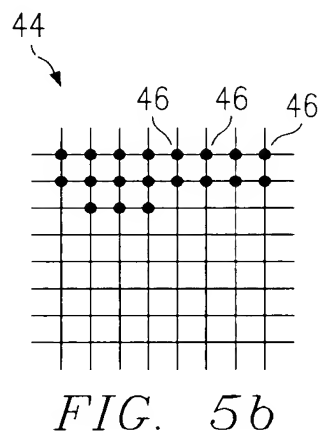
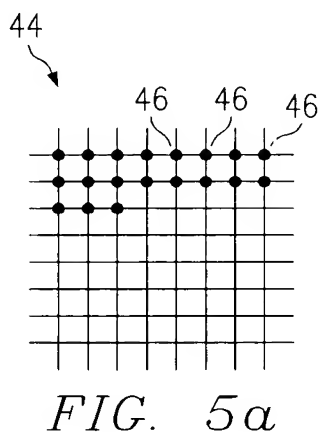
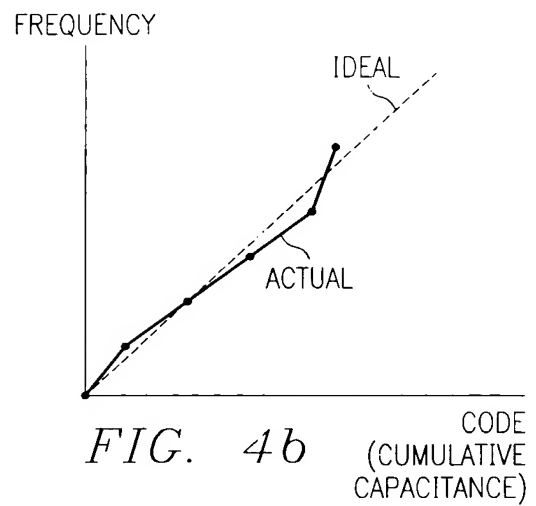
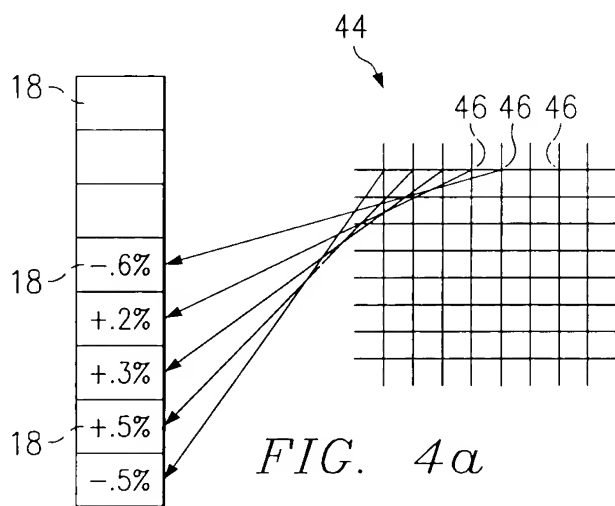


FIG. 3

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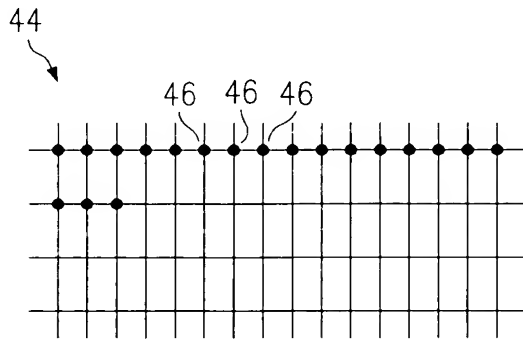


FIG. 7a

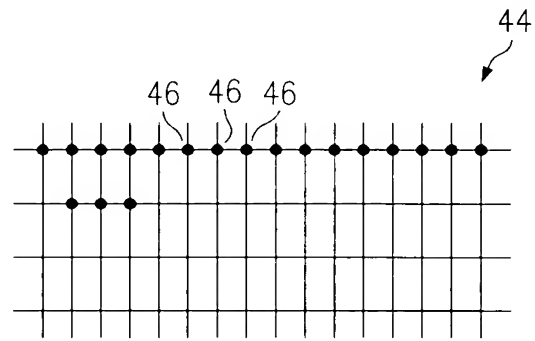


FIG. 7b

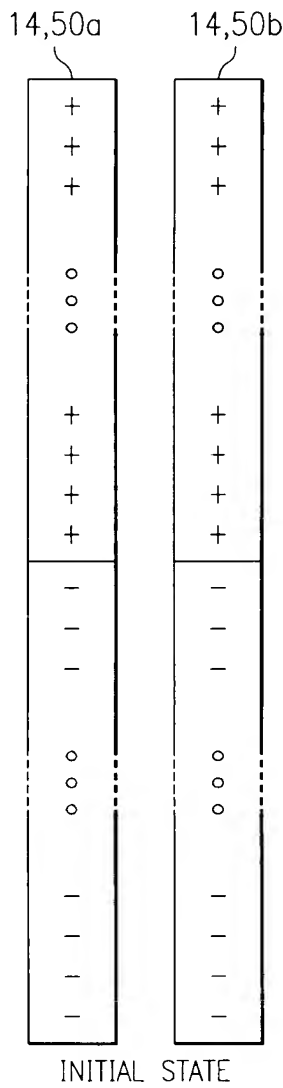


FIG. 9a

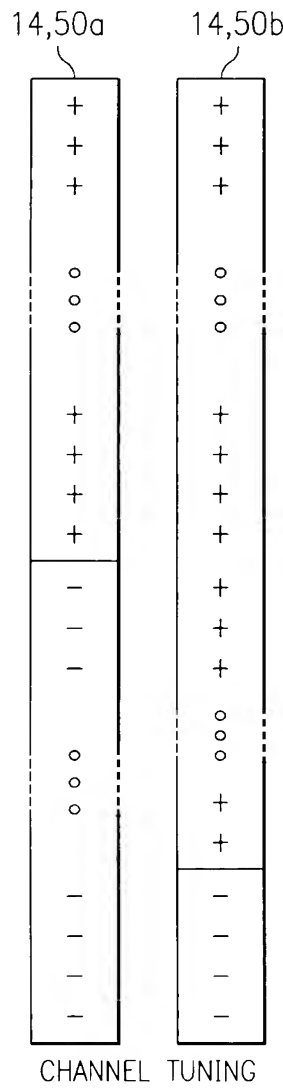


FIG. 9b

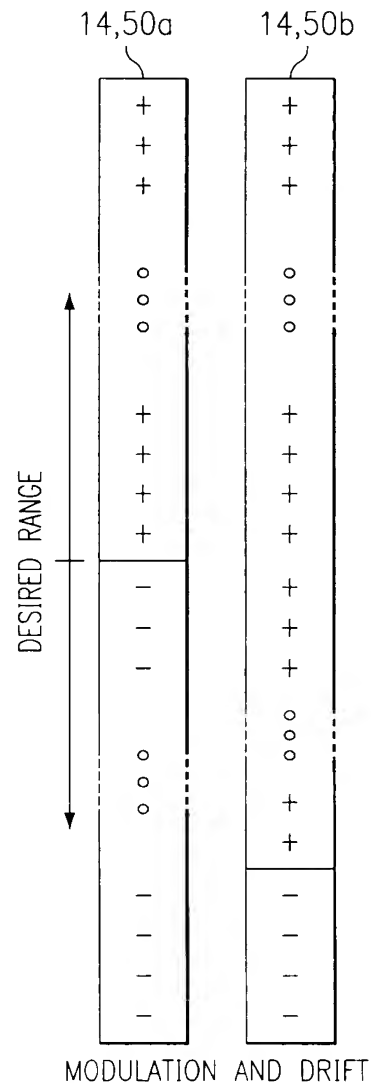
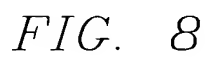
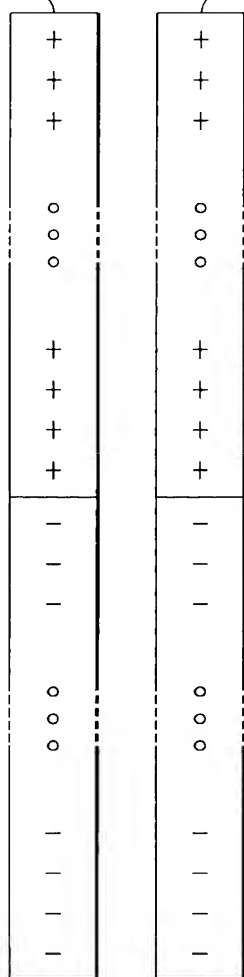


FIG. 9c



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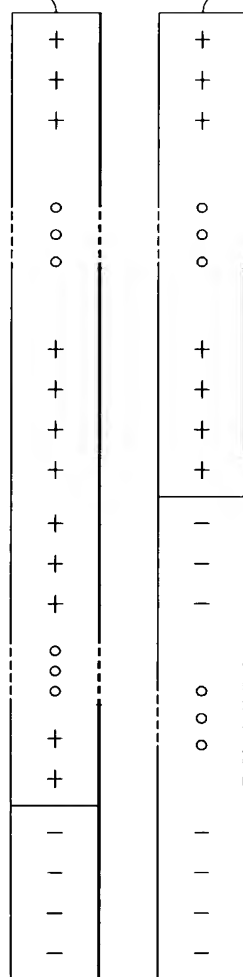
14,50a 14,50b



INITIAL STATE

FIG. 10a

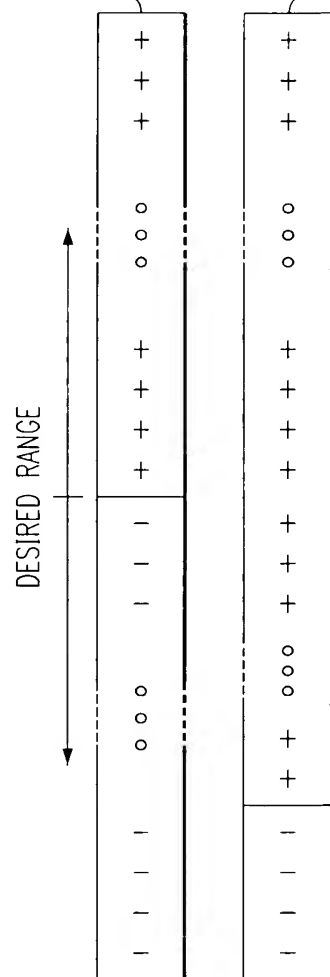
14,50a 14,50b



CHANNEL TUNING

FIG. 10b

14,50a 14,50b



MODULATION AND DRIFT

FIG. 10c

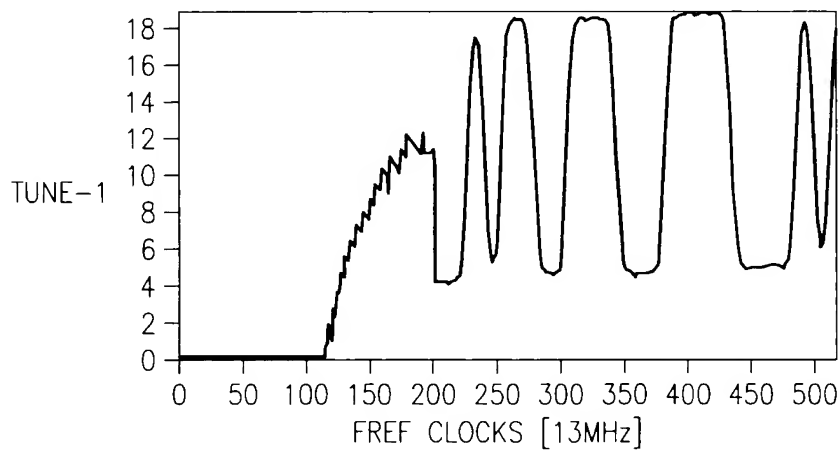


FIG. 11a

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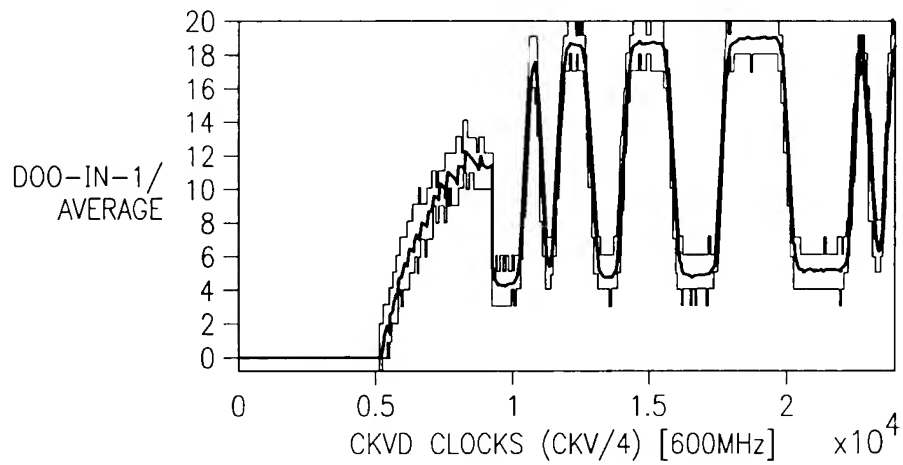


FIG. 11b

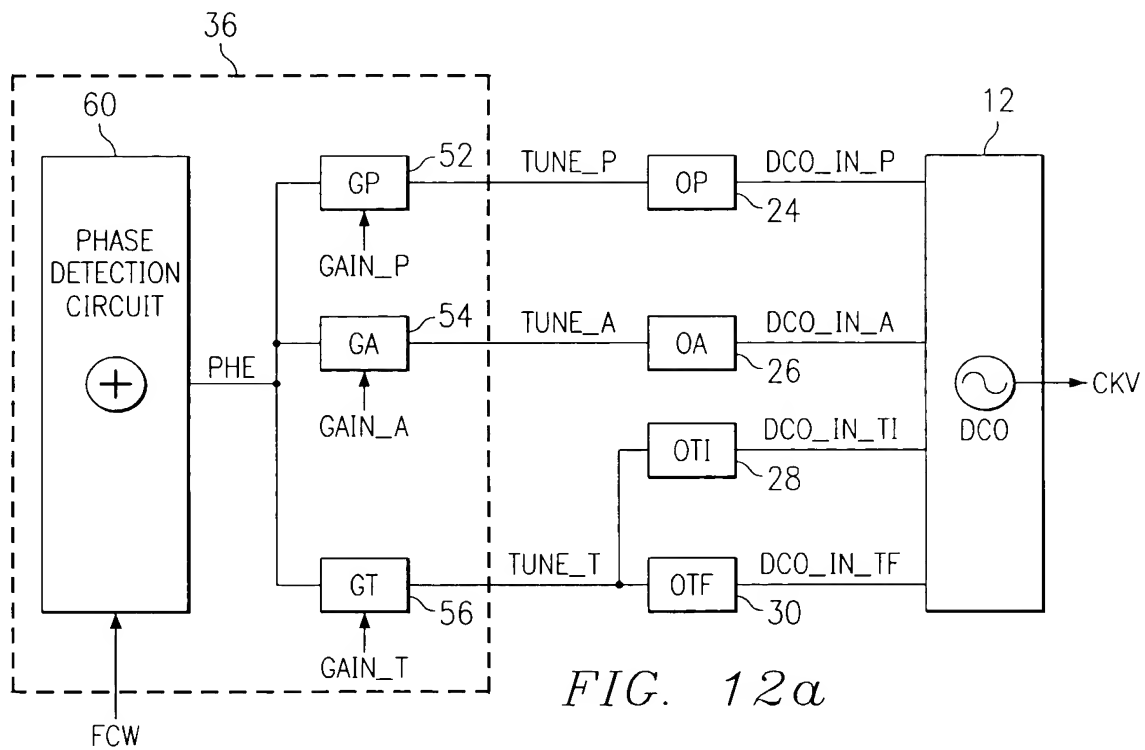
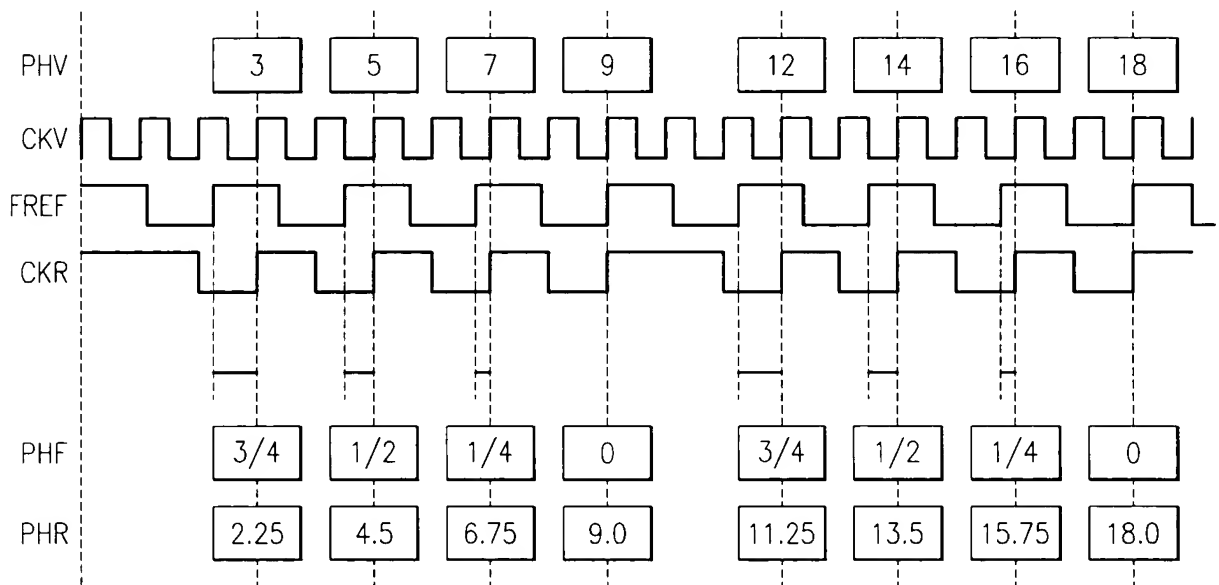
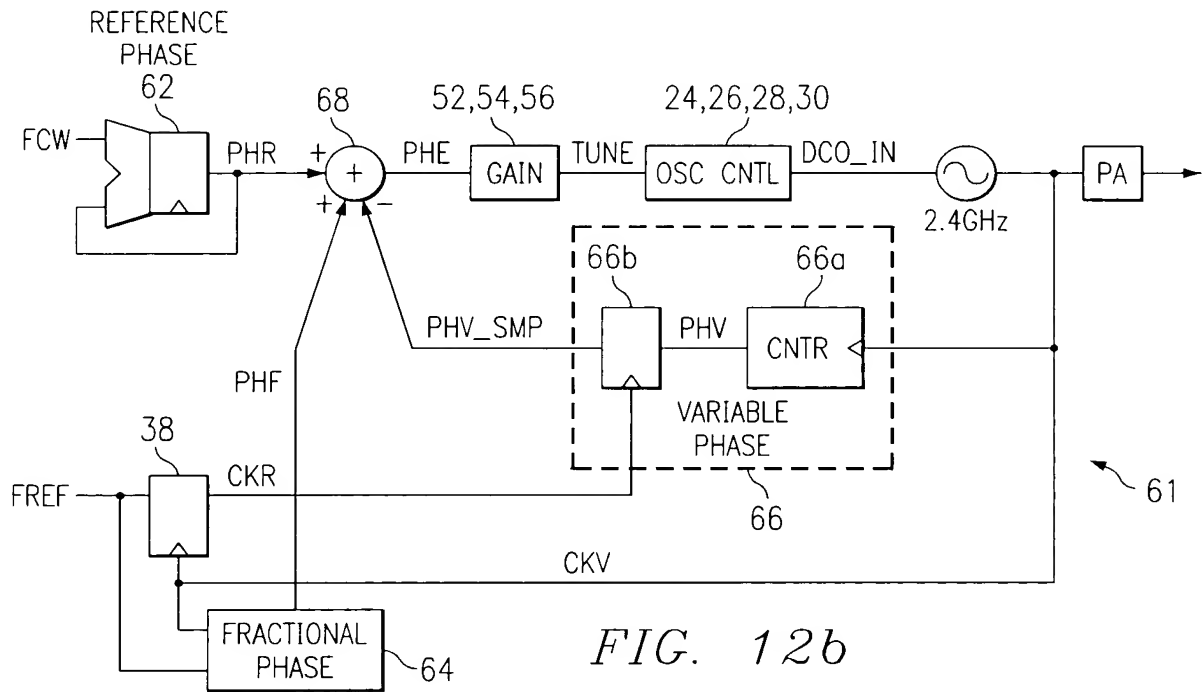


FIG. 12a



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FIG. 13

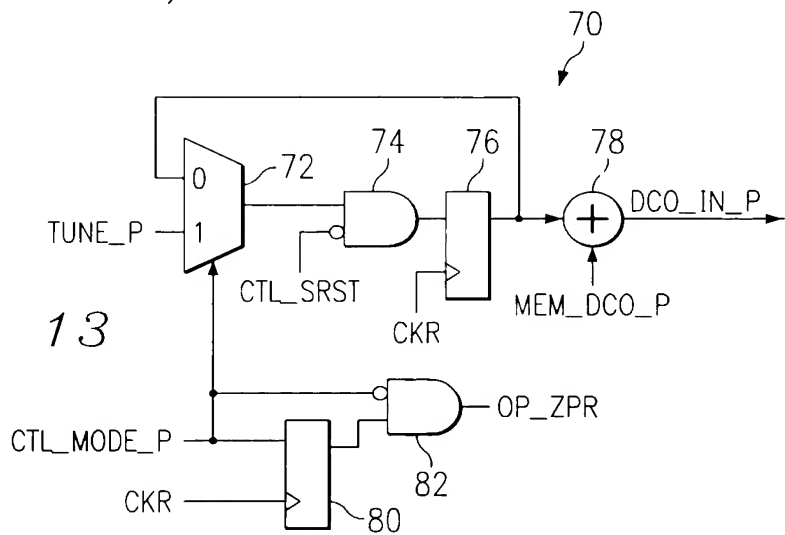


FIG. 14

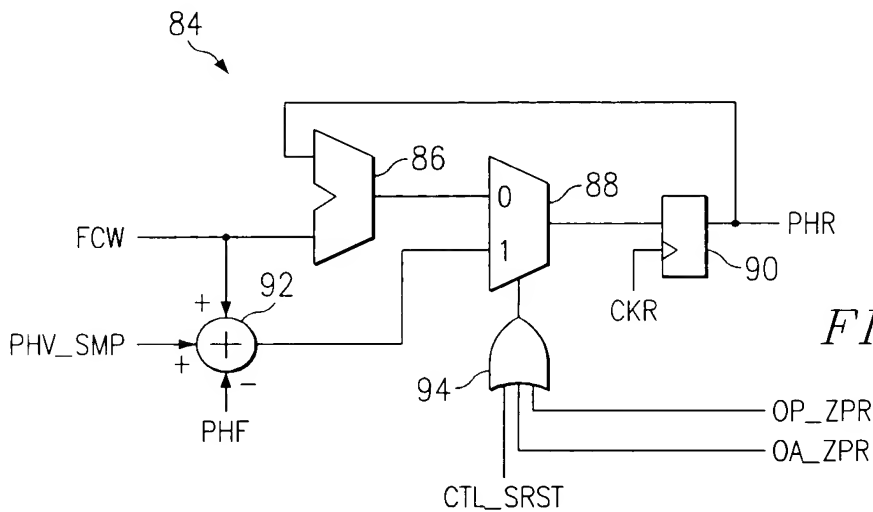


FIG. 15

